

PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL. NUM.	FILING DATE	CLASS	SUBCLASS	NAME	EXAMINER
10020C31	11/07/2001	438	107	2002	L. H. M.

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CONTINUING DATA VERIFIED:

FOREIGN APPLICATIONS VERIFIED:

EG-PUB DO NOT PUBLISH <input type="checkbox"/>	RESC'D <input type="checkbox"/>	ATTORNEY DOCKET NO. <input type="checkbox"/>
Foreign priority <input type="checkbox"/>	<input type="checkbox"/> yes <input type="checkbox"/> no	32931
USC 19 countries <input type="checkbox"/>	<input type="checkbox"/> yes <input type="checkbox"/> no	
United Kingdom <input type="checkbox"/>		
IT-E: Method of forming low CHC degradation MOSFET transistors		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.S.
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drawn	Page Drawn	Print Fig.
TERMINAL DISCLAIMER		Primary Examiner		
		PREPARED FOR ISSUE Application Examiner		
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